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NOTICE OF ALLOWANCE AND FEE(S) DUE

25297 7590 12/15/2009 JENKINS, WILSON, TAYLOR & HUNT, P. A.

Suite 1200 UNIVERSITY TOWER 3100 TOWER BLVD., DURHAM, NC 27707

EXAMINER				
KEBEDE, BROOK				
ART UNIT PAPER NUMBER				
2894 DATE MAILED: 12/15/2009				

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/550,040	06/28/2006	Mark Alan Lamonte Johnson	297/171 PCT/US	8058

TITLE OF INVENTION: METHOD AND SYSTEMS FOR SINGLE- OR MULTI-PERIOD EDGE DEFINITION LITHOGRAPHY

APPLN, TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$755	\$300	\$0	\$1055	03/15/2010

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 1SI. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FIEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or Fax (571)-273-2885

INSTRUCTIONS: This appropriate. All further indicated unless correcte maintenance fee notificat	form should be used to correspondence including d below or directed off tions	for trans ig the P nerwise	smitting the ISSU atent, advance or in Block 1, by (a	TE FEE and PUBLICATI ders and notification of r i) specifying a new corres	ON FEE (if requiaintenance fees vipondence address;	ired). I vill be , and/or	Blocks 1 through 5 s mailed to the current (b) indicating a sepa	correspondent rate "FEE AL	pleted where ze address as DDRESS" for
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Suite 1200 UNIV 3100 TOWER B		& н	JNT, P. A.	Lbe	Cer reby certify that th	tificate	of Mailing or Trans s) Transmittal is being ficient postage for firs ISSUE FEE address I) 273-2885, on the d	deposited wit	h the United an envelope ing facsimile elow.
DURHAM, NC	27707							(D	epositor's name)
									(Signature)
									(Date)
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10/550,040	06/28/2006		N	Mark Alan Lamonte Johnso	n	2	97/171 PCT/US	805	8
				MULTI-PERIOD EDGE					
APPLN, TYPE	SMALL ENTITY	ISS	UE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSU	E FEE	TOTAL FEE(S) DUE	DATE	EDUE
nonprovisional	YES		\$755	\$300	\$0		\$1055		03/15/2010
EXAM	INER		ART UNIT	CLASS-SUBCLASS					
KEBEDE,			2894	438-197000	•				
"Fee Address" indi PTO/SB/47; Rev 03-0 Number is required. 3. ASSIGNEE NAME A	ondence address (or Cha 3/122) attached. ication (or "Fee Address 2 or more recent) attack	inge of C " Indica ied. Use A TO BI	Correspondence tion form of a Customer E PRINTED ON T	2. For printing on the p (1) the names of up to or agents OR, alternativ (2) the name of a single registered attorney or a 2 registered patent atto listed, no name will be THE PATENT (print or type data will appear on the p I a substitute for filing an and the point of the part of	3 registered pater vely, e firm (having as a gent) and the nam meys or agents. If printed.	memb es of u no nam	er a 2 p to er is 3	ocument has b	een filed for
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NOTE: The Issue Fee and interest as shown by the r	d Publication Fee (if req records of the United Sta	uired) w ites Pate	rill not be accepted nt and Trademark	from anyone other than t Office.	he applicant; a regi	stered :	attorney or agent; or th	e assignee or o	other party in
Authorized Signature					Date				
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PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010. OMB 0651-0033



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JENKINS, WII	SON, TAYLOR & F	UNT, P. A.	KEBEDE	, BROOK
	ERSITY TOWER		ART UNIT	PAPER NUMBER
3100 TOWER B			2894	
DURHAM, NC 2	27707		DATE MAIL UD. 120 6000	10

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 0 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 0 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

Notice of Allowability

Application No.	Applicant(s)	
10/550,040	JOHNSON ET AL.	
Examiner	Art Unit	
Brook Kebede	2894	

The MALLING DATE of this communication appears on I All claims being allowable, PROSECUTION ON THE MERITS IS (OR REI netwith (or previously mailed), a Notice of Allowance (PTOL-85) or other NOTICE OF ALLOWABILITY IS NO	MAINS) CLOSED in this application. If not included appropriate communication will be mailed in due course. THIS This application is subject to withdrawal from issue at the initiativ
 This communication is responsive to <u>September 8, 2009</u>. 	
2. The allowed claim(s) is/are 15,16,26,31,34,38,39 and 41-50.	
Acknowledgment is made of a claim for foreign priority under 35 t a	ceived. ceived in Application No have been received in this national stage application from the mmunication to file a reply complying with the requirements
 A SUBSTITUTE OATH OR DECLARATION must be submitted. No INFORMAL PATENT APPLICATION (PTO-152) which gives reasor 	
CORRECTED DRAWINGS (as "replacement sheets") must be sub- (a) including changes required by the Notice of Draftsperson's Patt 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amend Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1.84(c)) sheach shets. Replacement sheet(s) should be labeled as such in the heade DEPOSIT OF and/or INFORMATION about the deposit of Bliattached Examiner's comment regarding REQUIREMENT FOR THI	ent Drawing Review (PTO-948) attached ment / Comment or in the Office action of ould be written on the drawings in the front (not the back) of r according to 37 CFR 1.121(d). DLOGICAL MATERIAL must be submitted. Note the
Attachment(s) . Notice of References Cited (PTO-892) . Notice of Draftperson's Patent Drawing Review (PTO-948) . Information Disclosure Statements (PTO/SB/08), Paper No./Meil Date . Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal Patent Application 6. Interview Summary (PTO-413), Paper No,/Mail Date 7. Examiner's Amendment/Comment 8. Examiner's Statement of Reasons for Allowance 9. Other
/Brook Kebede/ Primary Examiner, Art Unit 2894	

Art Unit: 2894

DETAILED ACTION

Allowable Subject Matter

1. Claims 15, 16, 26, 31, 34, 39 and 41 – 50 are allowed over prior art of record.

Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

After further search consideration of Applicants' amendment filed on August 8, 2009, it is determined that prior art of record neither anticipates nor renders obvious the claimed subject matter of the instant application as a whole either taken alone or in combination, in particular, prior art of record does not teach "wherein each of the semiconductor devices includes at least some of the adjacent, edge-defined, nanometer-pitched sidewalls that form the nanometerpitched channels and wherein each of the semiconductor devices includes a plurality of the nanometer-pitched channels," as recited in claim 15, " wherein each of the semiconductor devices includes at least some of the adjacent, edge-defined, nanometer-pitched sidewalls that form the nanometer-pitched channels and wherein each of the electromechanical devices includes a plurality of the nanometer-pitched channels," as recited in claim 16, "wherein the semiconductor device includes the channel; a fourth sidewall of nanometer-scale dimensions formed in the channel; fifth and sixth sidewalls of nanometer-scale dimensions on opposite sides of the fourth sidewall to form a mushroom-shaped structure; and wherein the mushroom-shaped structure comprises a gate material for a semiconductor device," as recited in claim 26, "at least one micrometer-scale feature being located in or on the semiconductor substrate; and at least one nanometer-scale feature being located in or on the micrometer-scale feature, the nanometer-scale feature being defined using edge definition lithography, wherein the micrometer-scale feature comprises a channel formed in the substrate and the nanometer scale feature comprises a

Application/Control Number: 10/550,040

Art Unit: 2894

sidewall located in the channel," as recited in claim 39, "at least one nanometer-scale feature being located in or on the micrometer-scale feature, the nanometer-scale feature being defined using edge definition lithography, wherein the micrometer-scale feature comprises a mesa and the nanometer-scale feature comprises a sidewall located on top of the mesa," as recited in claim 41, "at least one nanometer-scale feature being located in or on the micrometer-scale feature, the nanometer-scale feature being defined using edge definition lithography, wherein the micrometer-scale feature comprises a channel or hole being defined by the substrate and wherein the nanometer-scale feature comprises a channel located in a masking material deposited in the hole," as recited in claim 42, "at least one nanometer-scale feature being located in or on the micrometer-scale feature, the nanometer-scale feature being defined using edge definition lithography, wherein the micrometer-scale feature comprises a mesa located on the substrate and wherein the nanometer-scale feature comprises a channel located in a masking material deposited on the mesa," as recited in claim 43, "the second semiconductor material being different from the first semiconductor material; and a gate electrode being located on the substrate between the source and drain electrodes, the gate electrode being formed using edge definition lithography and wherein the gate electrode includes a first portion that extends outward from the substrate and a second portion that extends in a direction transverse to the first portion and overhangs the substrate," as recited in claim 44, and "a nanometer-scale emitter being defined on the base layer using edge definition lithography, wherein the emitter extends outward from the base layer," as recited in claim 50 respectively.

Claims 31, 38 and 45-49 are also allowed as being directly or indirectly dependent of the allowed independent base claim. Art Unit: 2894

Conclusion

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Correspondence

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brook Kebede whose telephone number is (571) 272-1862. The examiner can normally be reached on 8-5 Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kimberly D. Nguyen can be reached on (571) 272-2402. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications may
be obtained from either Private PAIR or Public PAIR. Status information for unpublished
applications is available through Private PAIR only. For more information about the PAIR system,
see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system,
contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like
assistance from a USPTO Customer Service Representative or access to the automated information
system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Brook Kebede/ Primary Examiner, Art Unit 2894